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No 3

January 79

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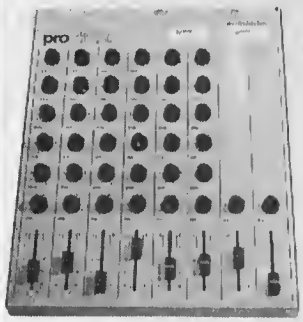

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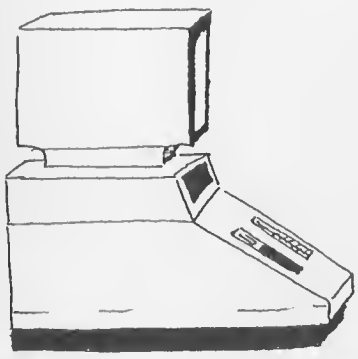
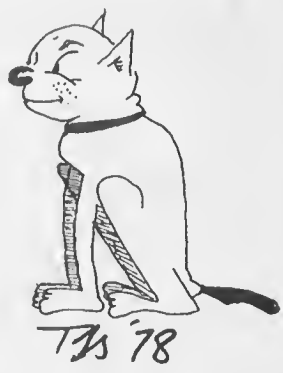
A number of errors that crept into last month's issue have been brought to our attention. Once again Phil Cornes was not credited in connection with the BASIC explained Series. Q1 in the EPROM Programmer should be shown as a 2N3638 and not PN3638 as in both Parts list and Circuit Diagram. The opto isolator is not a critical component and could be substituted by a more readily available device.

The missile program appearing in Softspot also contained a number of errors. Lines 90, 110 and 170 should be altered as follows

```
90 Y = (RND(16)—1)* 64;Z = 1
110 VDU Z,32
170 VDU@(1+2),32
```





Man's best friends

Letters

We would like to hear your views on computers and computing, we'll publish the best views in this, our new letters' page. This month, Mike Hughes — designer of the TRITON — answers a letter drawing attention to certain aspects of the computer design.

Dear Sir,

I am writing to request you to urgently consider the redesign of the Triton Computer, as it suffers from a serious design fault which will make it unreliable as it stands, and which will cause difficulties for expansion. As you may by now be aware I am referring to the data bus buffering. While the 8080a chip set has been buffered by a high drive (and unnecessary) 74LS245, the memory chips must also drive the data bus, and these have a single TTL drive capability. Bits 7 and 8 of the data bus are the most heavily loaded, with 5 LSTTL loads (= 1 TTL load), plus the UART, which presents 1 TTL load (at least this is what Texas say for the TMS 6011). Hence the memory chips are having to drive twice their rated load. Specifications being what they are this will usually be OK, especially when running so slowly. However usually is just not good enough in this situation, and several constructors are bound to get chips which are close to specification, and will be intermittent errors. The problem will become significantly worse when the bus expansion connector is used, as this will add an extra load, together with a lot of added cross-talk and bus noise.

As many of the people who may be considering the construction of this unit will not have the knowledge or equipment to detect this problem, I must, on their behalf, urge you modify this design as soon as possible.

A second, though less important, potential problem is in the -5V power supply, which is not a minor bias supply for 2708s (at least not according to Texas and Intel specifications). The specification is 30mA typical, 45mA max. Hence the power supply should be able to supply 180mA, which implies a series resistor of about 39-. This will dissipate a lot of power, and an IC regulator would be better.

Yours faithfully,
R. A. Cottis,
Corrosion and Protection Centre
UMIST

I cannot disagree with anything in Mr Cottis' letter. I am, in particular, grateful to him for identifying the -5V rail problem which was an oversight on my part.

Unfortunately, the values given for Rl and ZD1 were a "hangover" from an early prototype which operated with a single EROM. These should have been changed to accommodate the higher current consumption of the completed system. Mr Cottis is quite correct in saying that Rl should be 39 ohm with a power rating of 1.5W. At the same time, ZD1 should be upgraded to have a power dissipation of 1W. The latter is necessary in the event of the system being operated with only one EROM in position.

Fortunately (or unfortunately as the case may be) this error will not show itself in cases which operate with 3 EROMS whose current consumption averages just below the "typical" value. Not many people will, therefore, have experienced any problem. If they had the error would have been discovered earlier. Readers who have their system up and running with 3 Eroms in position need not react to the problem urgently — nevertheless, they should upgrade these components in due course prior to inserting a 4th EROM. Constructors just about to start are strongly advised to use the higher rated components from the word go.

The driving capabilities of the 2111 memories on the data bus is a much more difficult question to answer. Mr Cottis is quite correct in every thing he says by taking worst case input loads and output drive capabilities. The worst case conditions he mentions are, however, stated with a 0.4 or 0.45V maximum "O" level — allowing a 400mV low level noise margin.

Stretching the loading in the worst case condition will certainly encroach on the noise margin but as V_{OL} rises, as a result, so the input current will decrease. From figures available it would appear that in the worst case condition (including an extra LS load for an external bus driver), the noise margin could reduce to about 200 mV, and this would not be acceptable for stringent applications or in extremely high noise conditions.

As Mr Cottis implies, it is very unlikely that one will be so unlucky as to have all the worst case conditions stacked against him and this is what I was relying on to provide a reasonable noise margin for domestic/office noise environments. The "Bête Noire" is clearly the UART but I was reluctant to provide extra on board buffering for this for two reasons:-

1. It would have given rise to difficulties in layout — involving more board area.
2. This would have increased the board cost as well as possibly requiring extra components.

If one accepts that an extreme worst case conditions is unlikely there will be negligible problem at normal ambient temperatures. Even under a worse case situation at temperatures up to 60°C the system will still operate – but with impaired noise margin. It is in the latter situation that one might have problems with busbar noise etc.

I am indebted to Mr Cottis for drawing attention to this potential problem and in view of his comments, would suggest that readers planning expansions should keep their first umbilical cable from the main board to the peripheral mother board (soon to be published) as short as possible. They should also introduce a further bi-directional buffer on the data bus at the earliest position possible. This, incidentally, is already planned on the extension mother board.

To assure readers of the minimal chance of problems arising, we should point out that of the dozens of Tritons already built and working, there has not been one instance of bus noise problems. This includes one system which, already, has been externally expanded for a further 4k of RAM with no extra buffering.

M. J. Hughes

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TRS-80: A Small Business Application

TERRY JOHNSON owns, with a partner, an insurance brokerage firm with twelve branches spread across the country. The firm also has interests in various property. Having used a computer bureau to process various accounting information, the firm are at present installing a TRS-80 to provide all the bureau facilities plus additional services.

How did you start in business? About 20 years ago, after coming out of the RAF, where I was a night fighter pilot, I needed a job. I had no business background but managed to get a job with an insurance company. There I found out there were such things as insurance brokers and after a bit of a late night study, I set myself up as a broker. Things, like topsy, grew from there, at our peak in 1974 we had 18 branches, but have now cut back to 12 — We employ 10 managers and about 40 other staff.

Your first involvement with computers was via a computer bureau? That's right, about five years ago we decided that the volume of accounting work generated by the various branches was getting to a level where some form of automation was necessary. We approached a number of the recognised agencies to see if they could offer us the type of package we wanted, without exception the products they offered would have meant considerable changes to our systems — not at all what we wanted.

At this point we decided on a different approach, we found — through the yellow pages I think—a specialist computer firm which undertook to design programs to customers' requirements. The firm had a scientific background and had never tackled anything along the lines we were asking. They accepted the challenge and a short time later we had a FORTRAN program that did all we wanted — simply keeping track of money in and money out and of our commodity — insurance policies in our case — but it might just as easily have been nuts and bolts or oranges.

Did you encounter any problems with the system? No, everything went very smoothly, we prepared our own program cards on a second hand flexowriter we picked up for £1000 and 48 hours later we had our batch back. During that first year about the only thing that happened was that the system was improved to provide a profit/loss statement and balance sheet.

When did the Tandy TRS-80 come onto the scene? About the middle of the year but the first time I realised that an "in house" system might be within our reach was at the DIY computer show at the beginning of this year.

Why even consider a new system when your existing one was working so well? To save money. We calculated that a system costing around £2500/£3000 could save us money over a period of 5 years with, of course, all the benefits of in house computing.

Back to the Tandy then? — Yes, in April of this year I went to the States on holiday and after looking around at what was available off the shelf over there at that time I decided to buy a Tandy TRS-80. The machine I bought was a Level I 16K machine.

Any problems getting it through customs? Not after it had been classified as a data recording machine by binary system — no I paid my 7% duty on hardware and 8% VAT and I was through. I'd saved myself a lot of money. The cost in pounds was about £520. compared to the £700 odd that Tandy UK wanted assuming there was such a machine in this country.

What about servicing if the machine goes wrong? Tandy have a 90 day warranty on their equipment, which is available to first and subsequent owners in any country where Tandy have outlets — I wasn't worried about servicing.

Have you had any trouble with your machine? Yes, soon after I began using it, the keyboard suffered from an excessive amount of contact bounce. I took it to my local Tandy dealer, and it was back within a week — repaired at no charge under the warranty.

How did you get on with the machine in those early days? I had no experience of programming but found the Tandy manual soon had me familiar with BASIC and frustrated at the limitations of the level I machine.

I sent my TRS-80 to Tandy together with £79 for it to be upgraded to a Level 2 unit.

What was your next step? Well now came the most frustrating time for me, Level 2 BASIC was just what I wanted, but without a printer, floppy and more

memory the system just could not cope with the work I wanted it to do. Tandy had plans for expansion but no hardware available. I started looking around at other machines. The PET would never be acceptable to any typist with its present keyboard but the APPLE looked promising. I bought an apple this July but returned it about a week later, the reasons were that my machine kept crashing and as the BASIC I was using, Applesoft II was on tape, a five minute restart each time became trying. Add to this the fact that I was promised a number of hardware items that just didn't turn up and the Apple was not working out.

Back to Tandy? Yes. August was a dull month but I'd been promised the first expansion interface to arrive in this country and that arrived in September. The expansion interface contains space for an additional 16K or 32K RAM, a dick-controller for up to four disks, dual cassette decks as well as a Centronics parallel port.

After a bit of persuasion Sintrom at Reading — having sold me their ex-demonstration printer — hooked the printer up to the TRS-80, as the lead supplied did not produce any results this was a great leap forward — hard copy.

Things were moving fast now, as soon after this I discovered that a Micropolis disc drive would plug directly into the Tandy interface. I now had a system that could do all that I demanded of it.

Does this bring us up to date? No, a couple of weeks ago I went to the States again and picked up a couple of Tandy disks, a screen printer, a back up CPU and some extra memory. About $3000 worth, again a considerable saving on buying over here.

What about the software development? Of course that's been going on all the time, I've had no real difficulties with the task. The bulk of the work is done now, it's just a matter of getting the time to sort out the rest of the system for our target start date of January 1st.

Why should a small business man consider using one of these small computer systems? Three main reasons I think. Staff time and skill levels can be cut if a sensible system is devised. Accounting information that is good enough for our auditors to accept can be produced. Thirdly a vast flow of statistical information can be generated that gives the business man a far better idea of the performance of his operation at any point in time. What most people do by feel now can be done far more accurately by the computer. These three things together all add up to cutting costs.

Do you think small business men could cope with setting up a system such as yours without outside help? Most small businessmen are of above average intelligence — they have to be to survive. They also, in general have the drive and energy to get things done. I hadn't any knowledge of computers at the start of this year and reckon that now I have put together a system that will save me time and money. If I can do it I'm sure many other people in my position could. With the various software packages coming on the market at low cost, the business man may only be involved in a very small amount of work to get a low cost system to do exactly what he wants of it.

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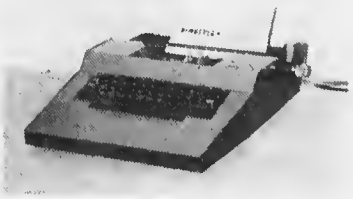

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TRITON Software —MONITOR

Mike Hughes takes a detailed look at the Tritons monitor and describes some machine code programs that can be run on the computer

If you have built the TRITON computer you will want to get the best out of it and there are several modes in which it can be used. You can write and run programs in BASIC but you may wish to record and recall these to and from tape. To do this you need to know something about the MONITOR. You may wish to use the computer for control purposes for which you need to write machine language instructions. To do this it is essential to go through the Monitor — possibly making use of some of its in-built sub routines.

For those learning about computers it is a very good idea to get a grasp of MACHINE CODE. It is not difficult to learn and can make life much more exciting as you will be able to get the computer to respond much more quickly than it will through a BASIC Interpreter. Sometimes you can get several thousand times the sped for certain operations. To do this on the TRITON you must operate through the MONITOR.

TRITON's standard MONITOR is a program written in machine code which is held in ROM starting at address location OOOOH. Its purpose is primarily to initialise the machine and to give it an elementary intelligence so that you can communicate with it. For this reason the machine must start off at the beginning of the monitor program every time it is switched on otherwise it becomes a worthless heap of rather expensive electronic components — unable to do anything. The first machine code instruction that the computer sees in the Monitor sets the STACK without which it would be impossible to do much in the way of decision making via nested sub-routines. It then proceeds to look at the next instruction which, in the case of TRITON, enables the interrupt operation if ever it is needed to be used. The following instruction is a JUMP which leads on to the first of the main routines called SCANMEM. This routine points to address 1600H and writes FFH into it. It then reads back the value from that address and checks that the FFH was actually stored. Furthermore it writes OOH into the same location and checks that too. This is a check that the memory is there and working which, at the same time leaves the memory location clear (i.e. containing OOH). The monitor routine then steps up one memory byte — to 1601H and does the same. This process is carried out on every successive location from 1600H upwards until the computer finds an error. The address of the location where the error occurred is most likely to be the top of the RAM work area but could be the address of a faulty IC. In either event this top address is written into a pair of RAM bytes used by BASIC 14.1 to tell BASIC how much work space is available. The two bytes of memory used for this are 1481H and 1482H. As a general rule whenever two byte instructions or data are written into memory the 8080 microprocessor expects to see the least significant byte in the lower value of the two addresses. Thus if 2000H was the location where memory ended — as found by SCANMEM, location 1481H would contain OOH while 1482H would contain 20H.

After checking the memory the monitor initialises the computer which acknowledges this on the screen with its standard message:

TRITON READY
FUNCTION? P G I O L W T

The Monitor then goes into a keyboard loop and the computer effectively waits for you to tell it what to do. This will depend on which key you depress. The letters it expects you to type are those shown in its acknowledgement and are abbreviations for seven different primary operations you can do with the monitor. These are:

P = Inspect any memory location and, if necessary change, or insert a byte of data. When the data is entered the computer automatically steps to the next address showing what is currently there and waits to see if you wish to change it.

G = Start running a program from any specified starting address location. The computer asks you, within this routine, what start location you want.

I = Input from tape recorder. The computer asks you for the header code of the file and then searches for it. When it has been found the data is written into the computer's memory starting at location 1600H. When the flag marking the end of the record has been found the computer re-initialises with an abbreviated form of its initial "switch on" message.

O = Output a program to the tape recorder. This, again asks you to give your recording a header code. The routine automatically outputs programs written in BASIC and stops when it gets to the end of file address (the address written into bytes 1600H and 1601H by BASIC). For user written machine code programs you have to manually enter the address immediately following your last instruction into these two bytes. Tapes are ALWAYS loaded and dumped with 1600H being the start location! When dumping has finished the computer re-initialises.

L = List the machine code content of all locations starting from any specified address. The

computer asks for the first address then prints out the contents of this and the next 14. It then asks MORE? and expects you to type Y otherwise it re-initialises.

W = Typewriter mode. The computer behaves just as if it was a keyboard and VDU. Anything you type is displayed on the screen including graphics. Cursor control and special VDU functions — e.g. Clear Screen, Reset Cursor etc all operate but the computer responds to nothing except CONTROL C which makes it re-initialise.

T = Jump to BASIC L4.1. This command causes the computer to jump out of the control of the Monitor into the control of BASIC. CONTROL C will jump back out of BASIC into the initialisation condition of the Monitor.

Note that CONTROL C will, in nearly all cases, get you out of an operation and back to the initialisation condition. The only times when it fails to do this are when you are locked in a user written machine code program loop; are searching tape for a non-existent header or are outputting to the tape recorder. In these three cases you will have to use Interrupt 2 (which re-initialises without clearing memory) or RESET which goes through the SCANMEM routine and erases any data in memory.

When the computer asks for its initial instruction via one of the above letters you simply have to type the letter. No carriage return is needed. If you type the wrong letter the computer replies "INVALID" and waits for you to try again.

Here are some examples to try with the above functions:

LED PORT TEST ROUTINE

1600	CALL	INCH	CD	Input data from keyboard to accumulator
1601	—		0B	
1602	—		00	
1603	CMA		2F	Complement contents of accumulator.
1604	OUT	PORT 03H	D3	Output contents of accumulator to
1605	—		03	PORT 3 (LED port)
1606	JMP	1600H	C3	Jump back to 1600H for next input
1607	—		00	from keyboard.
1608	—		16	

This program enables you to test out both the LED port and the keyboard by outputting the binary code from the keyboard to the LEDs. The program complements the accumulator to compensate for the fact that on TRITON the LEDs go on for level "0." In this program they will go on if a bit from the keyboard is "1." Because we are going through the monitor's INCH routine you will find that a shift inversion takes place. You get upper case alpha codes when unshifted and lower case alpha codes when shifted. This is designed in to make the keyboard more convenient to use. Numerical keys are not affected by the shift inversion. Again, because we are using INCH you can escape from this program loop with CONTROL C. You will notice that the most significant LED (bit 8) is permanently off; this is because bit 8 is used for the input strobe and this bit is masked off by the monitor as data is entered. Notice also that the data is latched on to the LEDs after the key has been released.

INTERRUPT DEMONSTRATION PROGRAM

1618	LXI D	161FH	11	Load start of string saying
1619	—		1F	IAM INTERUPT 3
161A	—		16	
161B	CALL	PSTRNG	CD	Print carriage return followed
161C	—		2B	by string.
161D	—		00	
161E	RET		C9	Return to main program.
161F	DATA	I	49	String data starts here and
1620	—	SPACE	20	terminates with end of text
1621	—	A	41	marker 04.
1622	—	M	4D	
1623	—	SPACE	20	
1624	—	I	49	
1625	—	N	4E	
1626	—	T	54	
1627	—	E	45	
1628	—	R	52	
1629	—	R	52	
162A	—	U	55	
162B	—	P	50	
162C	—	T	54	
162D	—	SPACE	20	
162E	—	3	33	
162F	—	EOT	04	

An example of a user written interupt routine using INT3 push button. Note that it starts at the re-vectored start address 1618:

Load this program then reinitialise and press INT3 button. Next press W and do a bit of screen typing and press INT3 from time to time. The interupt should announce its presence. We hope that this simple example will show you that, contrary to popular belief, interupts are quite easy to write programs for.

VIDEO TYPEWRITER PROGRAM

1600	LXI D	1612H	11	Load start address of message string
1601	—		12	in DE register pair. This is
1602	—		16	required by PSTRNG sub-routine.
1603	CALL	PSTRNG	CD	Call sub-routine which prints string
1604	—		2B	starting at address held in DE register pair.
1605	—		00	
1606	CALL	PCRLF	CD	Call sub-routine which prints out
1607	—		33	carriage return and line feed..
1608	—		00	
1609	CALL	INCH	CD	Call sub-routine which inputs data
160A	—		0B	from keyboard and holds this in
160B	—		00	accumulator.
160C	CALL	OUTCH	CD	Call sub-routine which prints out ASCII character from
160D	—		13	data in accumulator.
160E	—		00	
160F	JMP	1609H	C3	Jump back to address 1609H and wait
1610	—		09	for next character from keyboard
1611	—		16	
1612	DATA	0	4F	Start address of message string
1613		/	2F	
1614		K	4B	
1615		SPACE	20	
1616		T	54	
1617		Y	59	
1618		P	50	
1619		E	45	
161A		!	21	
161B		EOT	04	End of text terminator code.

When you run this program it acknowledges with the message O/K TYPE! and you can then use the TRITON as if it were in the W function operating mode (i.e. it becomes nothing more than a video typewriter). You can escape from the program by depressing CONTROL C (this applies to any program which repeatedly goes through the INCH routine).

ALPHABET TWELVE TIMES OVER USING I/O

```
1600  RSTI          CF   Clear screen via special restart instruction
1601  MVI B  0CH    06   Set decimal value 12 (oCH) in B to specify
1602  —             0C     number of alphabets required.
1603  MVI A  41H    3E   Set ASCII code for "A" in accumulator.
1604  —             41
1605  CALL   OUTCH  CD   Print contents of accumulator.
1606  —             13
1607  —             00
1608  INR A         3C   Increment ASCII code in accumulator by one.
1609  CPI    5BH    FE   Compare it with ASCII code which is
160A  —             5B     one greater than Z.
160B  JNZ    1605H  C2   If not greater than Z jump back to 1605H
160C  —             05     and repeat until complete alphabet is
160D  —             16     printed.
160E  CALL   PCRLF  CD   If alphabet is completed output carriage
160F  —             33   return and line feed.
1610  —             00
1611  DCR B         05   Decrement value in B register by one.
1612  JNZ    1603H  C2   If it is not zero we do not have 12 alphabets so jump back to
1613  —             03   1603H and repeat.
1614  —             16
1615  JMP    REINIT C3   If it is zero re-initialise.
1616  —             B9
1617  —             02
```

This program should be compared with the one following as they both do the same thing — print the alphabet twelve times and then re-initialise. This, first, program uses conventional I/O techniques whereas the second makes use of the powerful memory mapped option to the TRITON's VDU. We hope you will recognise the tremendous difference in speed of operation between the two methods. Note that in both programs we make use of the RST1 instruction at the beginning. This is one of 8 special re-start instructions which are fixed destination CALL instructions. Using RST1 will call the sub routine at location 0008H which, in the case of TRITON's MONITOR is then re-vectored with a jump to 0134H. The routine in question is the one which clears the VDU screen and resets the cursor. The advantage of using an RST instruction is that you do not have to specify the address of the sub routine hence saving two bytes in your program. You should only use RST instructions if the sub routine being called terminates in a RETURN command.

When you run the program try and judge the time it takes to display the 12 alphabets and then go on to the next example.

ALPHABET TWELVE TIMES OVER USING MEMORY MAPPING

```
1600  RSTI          CF   Clear screen with special restart instruction
1601  LXI H         21   Load HL register pair with address one less than start of
1602  —             FF   VDU RAM.
1603  —             0F
1604  MVI B  0CH    06   Set number of alphabets required in register B.
1605  —             0C
1606  MVI A  41H    3E   Set ASCII code for "A" in accumulator.
1607  —             41
1608  INX H         23   Increment HL register pair by one.
1609  MOV M,A       77   Copy contents of accumulator to memory.
160A  INR A         3C   Increment contents of accumulator.
160B  CPI    5BH    FE   Compare contents of accumulator with code
160C  —             5B     one greater than Z.
160D  JNZ    1608H  C2   If it isn't greater than Z jump back to 1608H and repeat.
160E  —             08
160F  —             16
1610  MOV A,L       7D   If it is: copy contents of L to accumulator
1611  ADI    26H    C6   Add 26H to this value.
1612  —             26
1613  CC     1629H  DC   If addition causes a carry; call sub routine which
1614  —             29   increments H register by one
1615  —             16
1616  MOV L,A       6F   Replace new low byte address in register L.
1617  DCR B         05   Decrement register B by one.
1618  JNZ    1606H  C2   If it's not zero we do not have 12 alphabets so jump back to
1619  —             06     1606H and repeat.
161A  —             16
161B  MVI B  0CH    06   Set decimal value 12 into B register.
161C  —             0C
161D  MVI A  0AH    3E   Set accumulator to ASCII code for line
161E  —             0A     feed.
161F  CALL   OUTCH  CD   Output line feed to VDU to step cursor down. (to get it
1620  —             13   clear of last alphabet).
1621  —             00
1622  DCR B         05   Decrement register B by one.
1623  JNZ    161FH  C2   If it's not zero we have not stepped cursor to below the
1624  —             1F   last line of alphabet so jump back to
1625  —             16
161FH and repeat
1626  JMP    REINIT C3   If it is; re-initialise.
1627  —             B9
1628  —             02
1629  INR H         24   Sub-routine to increment register H in event of a carry.
162A  RET           C9
```

Although this program is longer than the one just described you will see an element of similarity in the way the alphabet is formed (by incrementing the accumulator) and we keep track of the number of alphabets in the B register. Instead of using the I/O OUTCH routine we use the HL register pair to point to memory locations which are within the block of the VDU's RAM. This starts at 1000H and finishes at 13FFH. We then use the MOV M, A instruction which copies whatever is in the accumulator to the memory location being addressed by the HL register pair. By using the INX H instruction we can increment the latter to display the next character etc. Notice that carriage returns and line feeds are not needed in the main body of the program because we are using addressing to tell the computer exactly where to place each character. When one alphabet is finished we have to compute the address of the start of the next by adding the hex number 26 to the address currently in the HL register pair (this is done at instruction 1611H). Notice that after this operation we have to make allowances for a carry by calling an INR H sub routine.

When memory mapping the VDU you must remember that the clear screen/reset cursor operations does more than is immediately apparent to the eye. The addresses of different

positions on the VDU screen must correspond to specific places on the screen (1000H is the top left hand corner and 13FFH is the bottom right corner). If, as a result of previous activity, the VDU screen has been scrolling these absolute address values do not correspond to positions in an absolute manner you must carry out a cursor reset operation. This can be on its own or combined with the screen clear operation. A similar operation must be carried out if you use the VDU function when under the control of BASIC L4.1. Note that it takes the VDU 132mS to carry out a home cursor operation so if every you output the raw instruction in your own software you must introduce a time delay greater than this before outputting anything else to the VDU. The Clear Screen/Reset Cursor utility in the monitor has a delay of about 200mS built into it so you can call it without introducing any further delay.

Note that when this program runs the cursor stays stationary in its reset position (top left corner of screen). If we allowed it to stay there the re-initialisation message would overprint our alphabets so we have included some extra instructions (starting at 161BH) which step the cursor down twelve positions immediately prior to re-initialisation.

To further demonstrate the flexibility of memory mapping you can alter the layout on the screen by altering the value added to the address to get the next line. Alter the data at location 1612H to 29 and re-run the program. The lines should have start points staggered by 3 character positions. Similarly you can alter the start location for the display as a whole by altering the data in locations 1602H and 1603H. Try making these 20 and 10 respectively and alter the data at 1612H to 25. When you run the program the rows should slant the other way.

When memory mapping the VDU you must always be careful to ensure that your memory pointer (HL register pair) cannot exceed the highest address of VDU (13FFH) otherwise you will start over-writing the input buffer and stack area of the monitor. If this happens all sorts of strange things will begin to take place and you will probably find you completely lose control through the keyboard. If this should happen you must resort to the RESET button and end up with a cleared memory!

MEMORY MAP AND MONITOR UTILITIES

To help those who wish to get involved in machine code programming at an early stage here are the addresses of memory blocks and ports. It is assumed that you will be operating under the control of the standard Monitor program so we also list the addresses of its main Utilities.

MEMORY START ADDRESSES

0000H - 03FFH 1K EROM	(Holds standard MONITOR)
0400H - 07FFH 1K EROM	(Holds BASIC L4.1 "A")
0800H - 0BFFH 1K EROM	(Holds BASIC L4.1 "B")
0C00H - 0FFFH 1K EROM	(Spare location)
1000H - 13FFH 1K RAM	(VDU memory — can only be written into by computer)
1400H - 14FFH ¼K RAM	(Holds stack and tables for Monitor from 1400H to 147FH; 1480H upwards through this block will be used by BASIC L4.1 tables otherwise is free as M/C code work area)
1500H - 15FFH ¼K RAM	(Completely reserved for BASIC L4.1 stack otherwise is free for the user)
1600H - 1FFFH 2½K RAM	(Work area for BASIC L4.1 or user programs. NOTE that locations 1600H and 1601H are made use of by tape I/O routines to store End of File address — hence user programs for saving/loading to and from tape should always start at 1602H)
2000H - FFFFH 56K	Available for "Off Board" extensions

PORT DESIGNATIONS

00H	Keyboard INPUT (NOTE special routine is necessary)
01H	Tape I/O UART Status INPUT
02H	Tape I/O UART Data Strobe (start transmission) OUTPUT
03H	LEDs OUTPUT (NOTE LEDs are on for "O")
04H	Tape I/O UART Receive Data Enable (receive data) INPUT
05H	VDU OUTPUT (NOTE strobe — bit 8 — has to be specially formatted by software)
06H	Spare OUTPUT (NOTE only bits 7 and 8 are output)
07H	Relay OUTPUT (NOTE bit 8 is used to drive tape control relay out bit 7 is output as a spare line)
08H - FFH	Available for "Off Board" extensions

MONITOR UTILITIES ETC

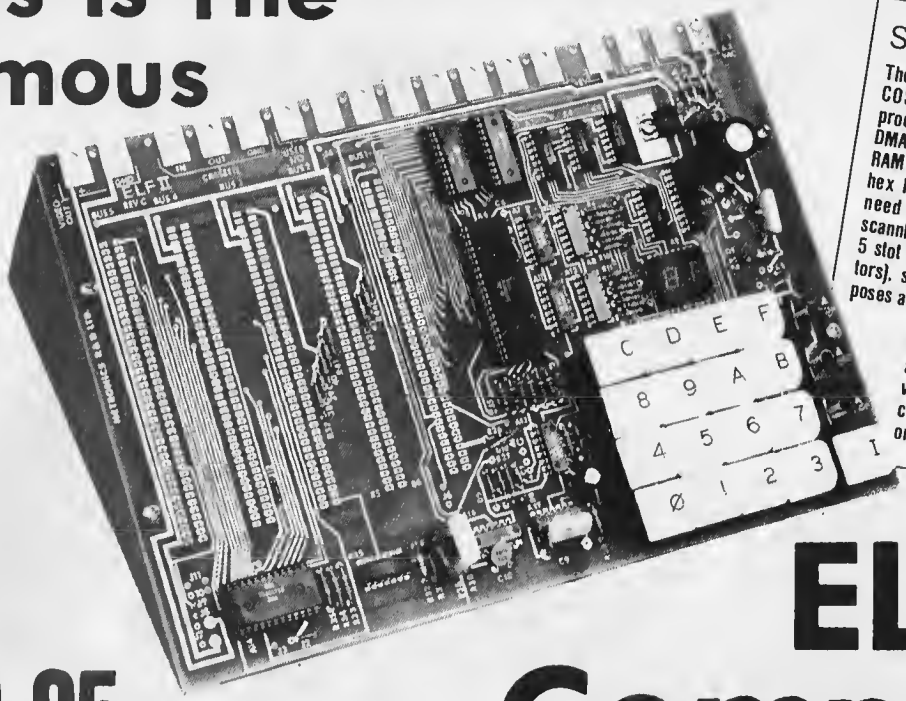
0000H RST0	Reset address. Enables Interrupt and checks and clears memory writing number of bytes available into locations 1481H (low order byte) and 1482 (high order byte). With full main board memory in place this should read 2000H. Then goes on to Initialise computer.

0008H	RST1	Reacts to Interrupt 1 clearing VDU and resetting cursor.
000BH	1NCH	A CALL routine which inputs character from keyboard. Stays in keyboard loop until key is depressed.
0010H	RST2	Reacts to Interrupt 2 clearing VDU and initialising computer without clearing memory. Used for normal reset operation.
.0013H	OUTCH	A CALL routine which outputs character to VDU.
0018H	RST3	Reacts to Interrupt 3 and re-vectors to 1618H for user routine.
001BH	INDATA	A CALL routine which allows string of characters to be entered to any place in memory. Start location of string pointed to by DE register pair which must be pre-loaded before calling 1NDATA. Returns on Carriage Return.
0020H	RST4	Reacts to Interrupt 4 and re-vectors to 1620H for user routine.
0023H	PDATA	A CALL routine which allows string of characters to be printed from any place in memory. DE register palr must be pre-loaded with start address before calling. Routine returns when it sees EOT terminator (04H) but DE steps to one address beyond terminator allowing immediate re-call for further string to be printed.
0028H	RST5	Reacts to Interrupt 5 and re-vectors to 1628H for user routine.
002BH	PSTRNG	A CALL routine identical to PDATA except that it outputs Carriage Return/Line Feed prior to printing string.
0030H	RST6	Reacts to Interrupt 6 and re-vectors to 1630H for user routine.
0033H	PCRLF	A CALL routine which prints Carriage Return followed by Line Feed and then returns with the original contents of accumulator intact.
0038H	RST7	Reacts to Interrupt 7 and re-vectors to 1638H for user routine.

The above are fixed location utilities whereas the following are within the main body of the MONITOR which are liable to be re-located if new generation monitors are written. They are, nonetheless, very useful and can save a lot of redundant instructions elsewhere. NOTE you can only guarantee using these if you have MONITOR V4.1 on board!

0053H	INIT	Destination of Interrupt 2. Clears screen and initialises computer. Also resets stack so should only be used as destination of a JUMP instruction.
02B9H	REINIT	This should only be used as a JUMP destination. It resets the stack and re-initialises the computer WITHOUT clearing the screen. A useful instruction at the end of user programs to avoid having to use the HLT instruction.
0327H	TPEON	When called switches the tape control relay on (relay contacts could be used for other control purposes by the user). NOTE that routine returns with 80H in accumulator.
032CH	TPEOFF	When called switches the tape control relay off. Routine returns with 00H in accumulator.
03A1H	ACKA	Start address of string which prints 1NVALID. Use should be made of PSTRNG utility to insert Carriage Return and Line Feed.
03B5H	ACKB	Start address of string which prints START=. (Use via PSTRNG).
03C4H	ACKC	Start address of string which prints HEADER=. (Use via PSTRNG)
03CFH	ACKD	Start address of string which prints END. (Use via PSTRNG)
00AAH	BASICIN	Checks to see if a key has been depressed on keyboard. If it has it returns with the data otherwise returns immediately with the accumulator cleared and flags set accordingly. Useful for real time interactive programs.
010DH	DLY	When called introduces approximately 3mS time delay and returns with all registers (except flags) intact.
0116H	F1VSEC	When called introduces a delay of between 5 and 6 seconds. Returns with registers and flags intact.
0134H	CLRSCN	Clears screen and resets cursor. Approximately 200mS time delay is built into this routine. Returns with registers and flags intact.

This Is The Famous



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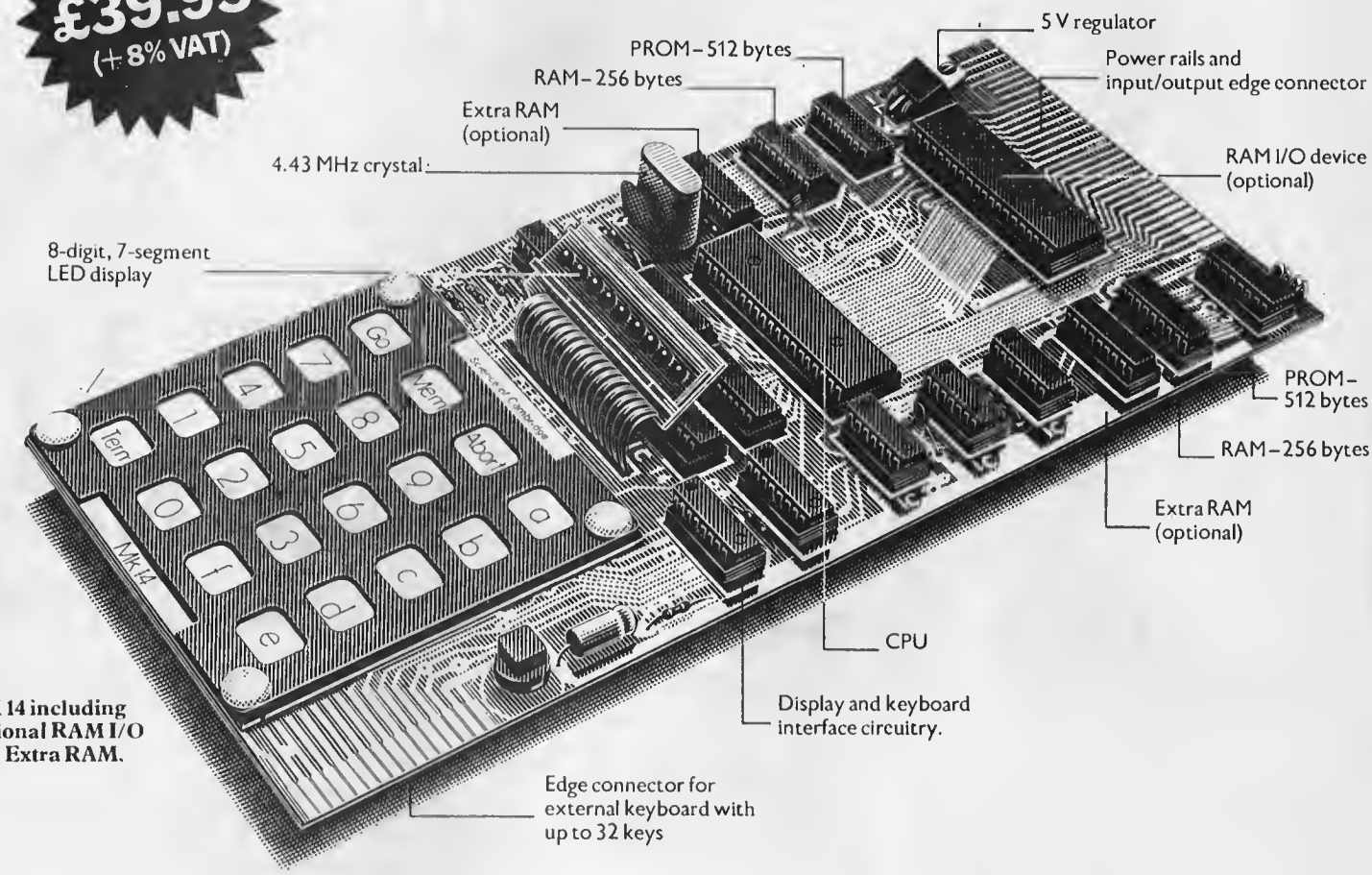

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And, of course, it's a superb education and training aid – providing an ideal introduction to computer technology.

SPECIFICATIONS

MK 14

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* 5 V regulator
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Edge connector access to all data lines and I/O ports

Optional Extras

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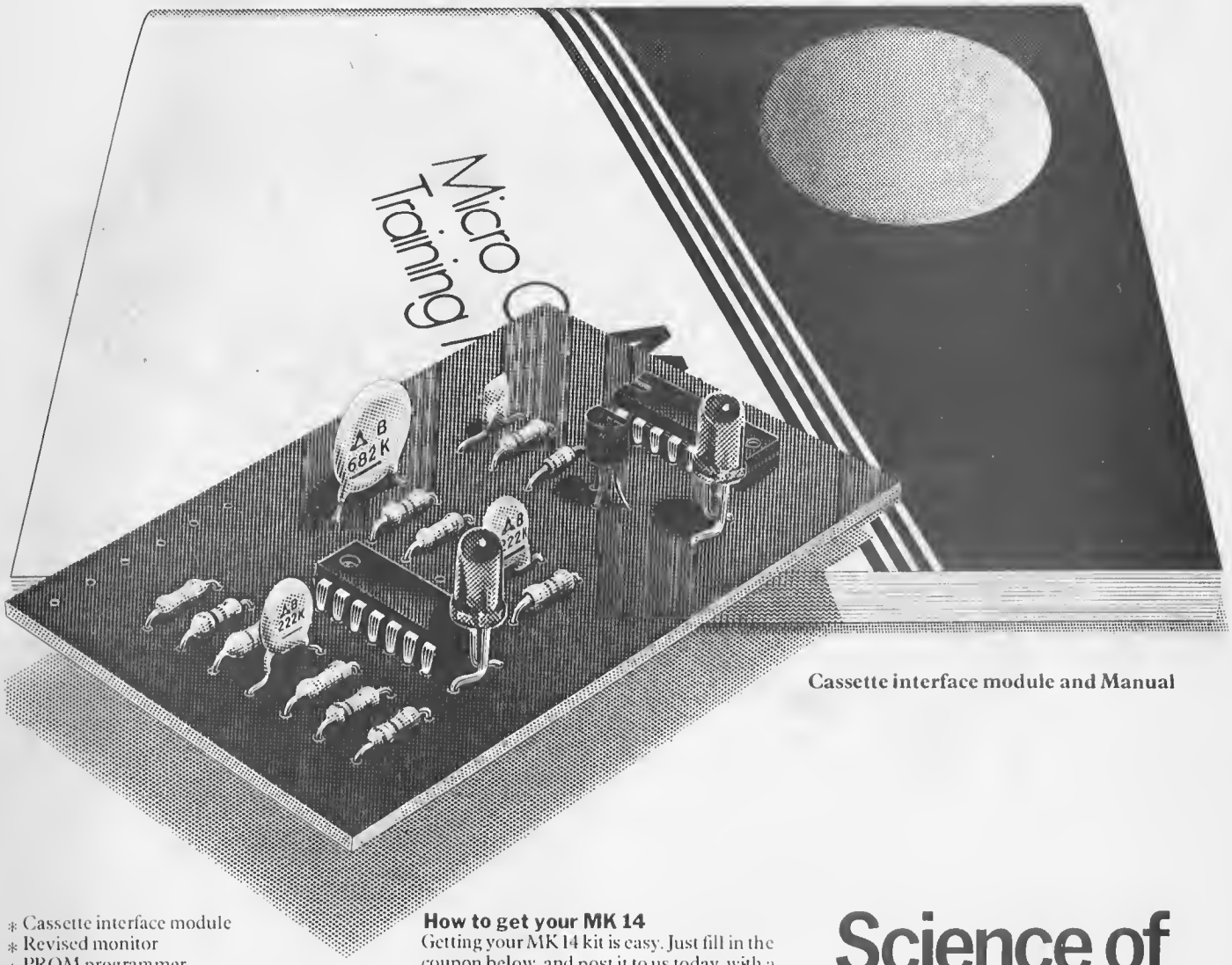

Cassette interface module and Manual

* Cassette interface module
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* PROM programmer
* Blank PROMS

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Every MK 14 Microcomputer kit includes a Manual which deals with procedures from soldering techniques, through programming and use of RAM I/O to interfacing with complex external equipment. It contains operational instructions and examples for training applications, and numerous programs including math routines (square root, etc), digital alarm clock, single-step, music box, mastermind and moon landing games, self-replication, general purpose sequencing, etc.

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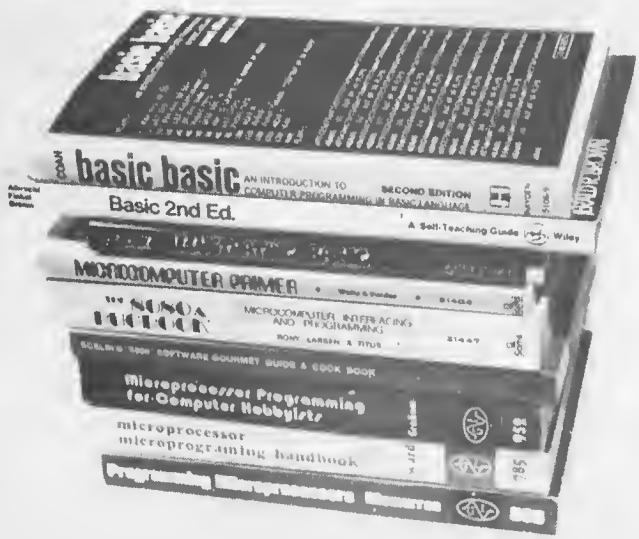

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BASIC Programming **£6.10**
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TV Typewriter Cookbook **£7.40**
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Microprocessors — Technology, Architecture, and Applications **£8.00**
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Microprocessor and Microprocessor Systems **£20.50**
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A completely up-to-date report on the state of the art of microprocessors and microcomputers, written by one of the leading experts. It thoroughly analyzes currently available equipment, including associated large scale integration hardware and firmware.

The 8080A Bugbook: Microcomputer Interfacing and Programming **£7.60**
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The principles, concepts and applications of an 8-bit microcomputer based on the 8080 microprocessor IC chip. The emphasis is on the computer as a controller.

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DESCRIPTION

The REVERSAL GAME is a game of skill played with the computer. The computer will arrange the numbers 1 to 9 in a random sequence and your job is to arrange them into ascending order in the minimum number of moves.

For example

The machine might print the following sequence –

8 3 7 9 1 4 2 6 5

You now have to get these arranged in ascending order. To do this, you are allowed to reverse the order of any number of digits starting at the left hand side. The machine will ask

NUMBER TO REVERSE

If you enter 4 the result will be

9 7 3 8 1 4 2 6 5

If you now enter 9 you will get

5 6 2 4 1 8 3 7 9

You now have 9 in the correct place and can set about getting 8 7 6 etc. in their correct positions.

```
 10 PRINT 'THE REVERSAL GAME'
 20 FOR I = 1 TO 9
 30 LET A = RND (9)
 40 IF 1 = 1 GOTO 80
 50 FOR J = 1 TO 1 - 1
 60 IF @ (J) = A GOTO 30
 70 NEXT J
 80 LET @ (I) = A
 90 NEXT I
 95 LET B = 0
200 PRINT #2, @ (1), @ (2), @ (3), @ (4), @ (5), @
(6), @ (7), @ (8), @ (9)
210 INPUT 'NUMBER TO REVERSE' J
220 IF J < I GOTO 240
230 IF J < 10 GOTO 260
240 PRINT 'INVALID – TRY AGAIN'
250 GOTO 210
260 LET K = (J+1)/2
270 FOR I = 1 TO K
280 LET A = @ (I), @ (I) = @ (J+1-I), @ (J+1-I)
= A
290 NEXT I
295 LET B = B+1
300 FOR I = 1 TO 9
310 IF @ (I) # I GOTO 200
320 NEXT I
330 PRINT 'TOTAL', #3,B
340 GOTO 10
```

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Personal Computing — The Early Years

Tip toe through the early memories of the Personal Computing field

In The Beginning

It all started back in 1974 when Intel introduced the 4004, the first true microprocessor. It developed almost by accident, as a result of Intel's efforts to produce a calculator of unprecedented flexibility. The shock waves of the hand-held calculator revolution were still being felt by every section of society and the back-room boys' eyes lit up with that "You ain't seen nothing yet" look as they drew up the chip-masks for their next product, the 8-bit 8008.

Assisted by the lessons learned from the cut-throat calculator business the microprocessor developed with frightening speed and predictability. Frightening not only because of the vast amount of high-technology and high finance poured into the field, but also because of the dramatic effect extrapolations of such technology can have on a broad spectrum of society. Predictable because everyone knew what was going to develop. The shrinking of calculators from giant cabinets to flip-top packs in the space of just a few years created an extraordinary blase attitude towards electronics. It was a spectacular demonstration of the omnipotence of the new technology of micro-electronics. It was proof that now nothing is impossible — just draw up the specifications, expend x thousand man-hours and y million dollars, and there you have it. So the microprocessor has been born at a time when nothing comes as a surprise any more. But maybe the real surprise is yet to come.

The New "Hams"

Of all the sub-sets of electronics hobbyists the most clearly defined to date has been the radio amateur. Members of this particular sect follow a technological faith which started with the first wireless communication and has since flourished, gaining millions of followers in a relatively short time.

For many hams their hobby almost becomes a life-style within itself, always striving for that rare DX the eternal pursuit of that elusive one-to-one SWR. Is it possible that we are now witnessing the founding of a new faith, one whose god speaks in 1s and 0s rather than 5s and 9s?

By making a few comparisons between amateur radio and amateur computing certain patterns can be seen emerging which may be an indication of what course the future of personal computing might take.

Power To The People

Like amateur radio, amateur computing is a high-technology which makes the latest developments in the science of electronics available to anyone at all who has the time and money to pursue them. The money factor is all important — the lower the cost of the hardware, the more people can afford to pursue the hobby. A reasonably useful microcomputer system might cost in the order of $1,000. Hams might spend this sort of money on radio gear, for that matter a radio-controlled aircraft enthusiast, amateur photographer or stamp-collector could easily spend that much on his hobby. So thanks to the microprocessor the cost of your own personal computer is no longer a barrier to most people.

This new accessibility and the free interchange of ideas and information between hobbyists has the effect of distributing "computer power" over a broad spectrum of people. This leads to a breaking down of much of the mystique which has traditionally surrounded the world of computers; they are being de-mystified as the magic is systematically exposed as little more than sleight of hand. As large number of amateurs invade a hitherto sacred field which was once the sole province of a privileged few the elite will inevitably grow in number until it finally becomes plebian.

"Homebrew" vs "Appliance"

As with amateur radio there are two factions within the computer cult, the "homebrewer" who builds his own equipment for the sake of the experience gained, and the "appliance operator" who buys a ready-built, going unit and gets what he wants from operating his instrument, writing programs and experimenting with the performance of the hardware as bought. His investment in the computer itself is more financial and less emotional than in the case of the homebrewer.

There is always some overlap between the two factions, but they can usually be classified by comparing the time spent building, testing and modifying the hardware to the time spent actually using it once it's working.

The Sky's The Limit

Radio equipment has rather unique and interesting characteristics. It can never really be declared "finished". There is always more to add to the station, improvements to be made, better antennas, higher power, lower noise. Computers share this trait which makes them too the ideal subject for a hobby. Today's mass storage is tomorrow's scratch-pad. There is unlimited scope for improvement and expansion of the hardware.

If ever the computer itself should look like having its full complement of RAM, ROM. AND I/O parts, the hobbyist can turn his attention to the vast range of peripherals that are available to him. A radio transmitter can be hooked up to an antenna and a microphone and that's about it, but nothing can be so insular as to resist interfacing to a computer if the intrepid hobbyist uses a little imagination.

More importantly, once the computer is operative a literally infinite amount of software development waits to be done. Like radio operating, this phase of the hobby is particularly attractive because the operating cost is nothing more than the electricity bill.

The Junk Box

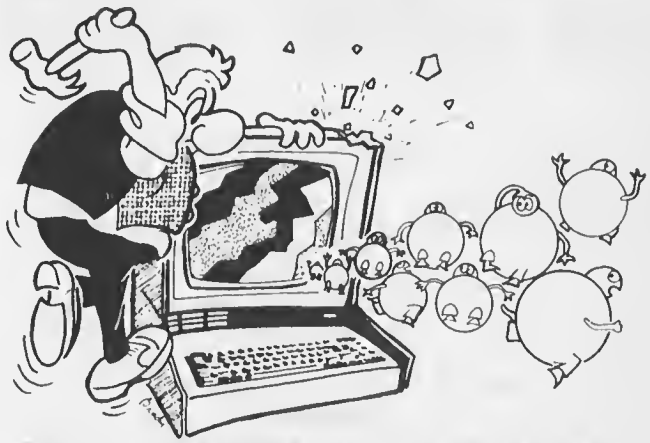
Ever since the tradition of stripping a discarded radio chassis was established by the pioneers of amateur radio, the humble junk box has been the hallmark of the truly worthy hobbyist. In much the same way as one may judge someone's social standing by the way he dresses, how neatly his garden is kept, radio amateurs assess each other's status by the quality and quantity of a bits and pieces which lurk for years in the dark recesses of their junk box until their true worth is finally recognised and they are discarded. Because the microcomputer hobby is so new, junk box computer parts of good vintage are rarer, but there is always the stimulating challenge of pushing a seemingly irredeemable piece of obsolete equipment into service. Radio ham and computer hobbyist alike share the unique pride and joy of operating equipment which the professionals have officially declared worthless.

Doing The Impossible

Besides the resurrection of dead equipment, hams are keen on performing another type of miracle. This involves proving by practical demonstration that something which should by rights not be possible does, in fact, work. With amateur radio this usually entails forging forth into extremes of technology (or bad practice, depending on how you look at it), generally revolving around a successful communication in spite of a red-hot "final", vast distances or an antenna made of wet string.

To the computer ham comparable feats entail successful execution of programs which are either exceptionally short or unbelievably long or so cunningly convoluted that not even the person who wrote it knows how it works. Thanks to the new technology involved there is also a whole new set of miracles which rely on getting a phenomenal number of logic functions into an incredible small space.

Amateurs are in a rather unique position in that they are permitted to exceed manufacturers' ratings to see to what limits they can push a particular component or piece of equipment. This practice gave rise to many novel techniques in the field of radio and a similar thing is bound to happen in computing.



The software bugs seem to come out just before sunrise.

Time Is Not Money

Amateurs make many other contributions to the science to which they are devoted as a result of the enormous amount of time they spend on their hobby. Because of the non-commercial nature of their pursuits, computer hobbyists can afford to undertake time-consuming projects which would not be economical as a professional enterprise.

Like the radio amateur who stays awake all night tuning across the bands looking for a rare contact, the computer ham often burns the midnight oil chasing an elusive bug in his software. Radio propagation never seems to be optimum at a civilized hour; similarly the software bugs only seem to come out just before sunrise.

With both amateur radio and amateur computing the real fun of the hobby lies in setting a goal and then achieving it no matter how long it takes or how inefficient the techniques used may be. The computer ham may devote hundreds of hours to developing a program that does nothing more than play a seemingly useless game. But, as with any technical hobby, a lot of valuable techniques are learned in the process.

Spreading The Word

A natural development from any widely followed hobby is the formation of clubs where people with similar interests can meet and exchange ideas. Major amateur radio clubs like the Radio Society of Great Britain, the Amateur Radio Relay League and the Wireless Institute of Australia have been established for many years and cater for hundreds of thousands of enthusiasts.

Even though the do-it-yourself computer hobby is

so young there are already hundreds of computer hobby clubs. The biggest of these are found on the west coast of America which is where most of the world's microprocessor products originate. The Southern California Computing Society has about 5,000 members. At the moment there are nearly 200 smaller computer clubs in the USA and an estimated 20,000 people have their own personal computer.

Magazines devoted entirely to the computer hobbyist have been established with great success. The most widely read glossy is **byte** which now circulates over 60,000 copies.

The radio amateurs' "field day" has always provided a means of information exchange between individuals. As communication is the basis of ham radio, publicising such events poses no problems, but computer hams have only their specialist magazines for such promotion. A few conventions have been held by computer hobbyists where the main purpose has been to establish standards so that hobbyists can easily share the software they have developed. Manufacturers of personal computing hardware also take an interest in these gatherings because it is an excellent opportunity to find out what the hobbyist is interested in and therefore which products will sell.

Speaking of Computers

Due to the unusually verbal nature of the hobby itself, radio amateurs have developed a unique vocabulary. The language which results has such a high jargon content and is spoken so fluently that it is quite unintelligible to the outsider. This serves to give the group its own identity and binds its members together.

Although amateur computing is still in its infancy its followers found that the computer industry had already provided them with a highly developed jargon, complete with an impressive range of off-the-shelf, buzzwords which have been nutured to perfection by 20 years of professional verbal dazzling. This they have eagerly seized and followers now have a language of their own.

The most telling sign of both radio and computer hams is their often amusing ability to construct seemingly meaningful sentences using all the rules of English grammar except that the keywords are replaced with strings of number of initials. The radio amateur might say, "QRX, I've got to check my SWR", while the computer amateur could hit you with, "I've put a PIA on my 6800 for I/O."

To the uninitiated talking in code like this seems like an awfully anti-social way of passing secret messages between club members — it serves to keep the in-group "in" by providing a feeling of comradeship for members and it keeps ot all but the most determined newcomers.

Future Shock (Electric)

Although personal computing is already well established as a hobby, the real impact of its advent is yet to come.

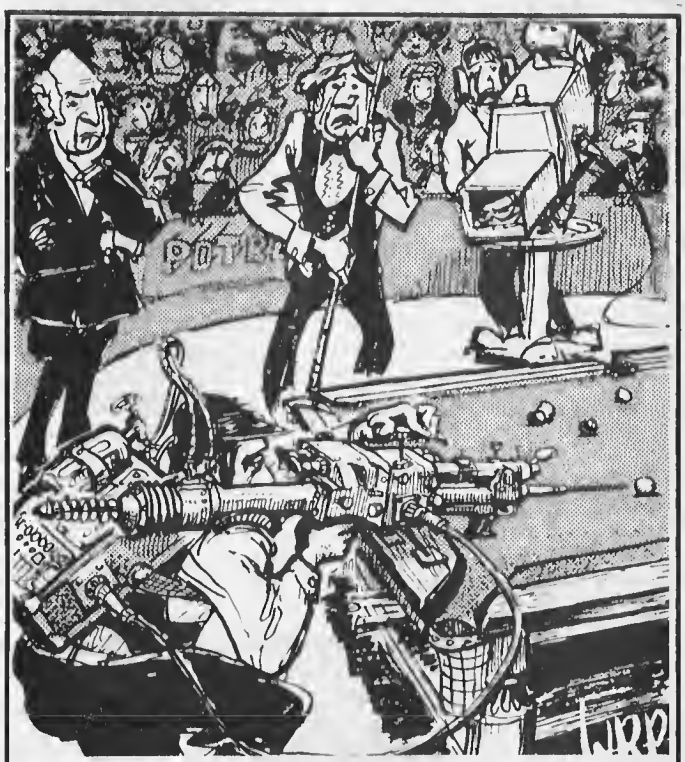
It is a characteristic of any hobby that those who pursue it develop great expertise in the field.A keen 10 year old stamp collector may know as much about stamps as a professional stamp dealer. Having spent his youth building radio transmitters a ham of 20 might know as much about radio as a University-qualified electronic engineer.

We are now finding a new breed of hobbyist/expert, a hobbyist who has spent thousands of hours of leisure time building computers and programming them. He could well know more about computers than many professionals in the field. As the hobby grows there will be more and more people to whom computers are second nature, people who are fully conversant with a broad range of computer concepts and totally up-to-date with the state of the art.

Traditional training and qualifications are already being seriously challenged by these hobbyists who might enroll in a University computer science course already knowing more than they will be taught.

As this flood of expertise hits the workforce we are bound to see dramatic changes in the status of the computer professional. Will there be a sudden surplus of computer engineers and programmers, or will the wave of new technology bring with it expansion of the industry to absorb it?

The remarkable advances in solid state technology which led to the development of microprocessors have made their mark on the electronics industry, but it's the "expertise explosion" which will follow that will have the real impact on society.



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TRADE ENQUIRIES WELCOME

TRS-80 Update

The August issue of ETI carried a reciew of the Tandy's TRS-80. The machine, made available for review, was equipped with the Tandy Level 1 floating point 'Tiny BASIC' ROM and 4K of dynamic RAM.

Since then Tandy have introduced a number of developments on the BASIC TRS-80 machine — The Level 1 system was made available with 16K of memory and the more powerful Tandy Level 2 BASIC was introduced into this country supported by both 4K and, top of the range, 16K of dynamic RAM. Any machine from the range can be upgraded in performance by returning it to Tandy for internal mods.

This update to the original review deals with the major differences between machines equipped with the Level 1 and Level 2 BASICs. For a general description of the TRS-80 machine, the comments of the original review are still valid.

Key Improvement

The level 2 BASIC is a 12K version of the microsoft BASIC used by many machines in the TRS-80's price bracket. Before going on to describe the Level 2 BASIC, which is considerably more powerful than Level 1, in detail, it may be as well to mention two hardware orientated improvements implemented on this upgraded machine.

The TRS-80's keyboard is decoded by the resident software and in the Level 1 machine the decoding system used meant that one had to release one key before the computer would allow the entry of another. This led to such messages as "Plase tye in yor name" — Even those of us who are not touch typists could get up enough speed to defeat the Level 1 keyboard. Level 2 lets you hit the second key before you have released the first key. Even level 2 does not let you get away with too much, however, and if you hold down three or four keys at a time, some keys will then generate several characters as they are pressed.

The other major area of improvement on the hardware/software border is the speed at which programs are dumped to and loaded from the TRS-80's cassette recorder.

Level 1 used a data transfer rate of 250 baud — the level 2 rate is twice this at 500 baud. Because of this faster transfer rate it may be found that the volume setting on the recorder that was suitable for use with a Level 1 machine will have to be altered for satisfactory performance with Level 2, in general a lower volume control setting will be needed.

Editing

One of the major differences between Level 1 and Level 2 is the provision of a powerful editing facility in the latter. We do not have enough space to go into a detailed description of the editor but a few examples of what can be accomplished should give an idea of its power.

One can list any line of a program individually, insert material anywhere in a line, delete material anywhere in a line, delete the remainder of a line beyond the cursor and insert new material in its place. Any desired number of characters to the right of the cursor may be deleted or a change may be made to a specified number of characters. The editor provides a search facility, so that (for example) a line may be searched for the second occurance of the letter G and move the cursor to that position or tell the computer to delete all characters to that point and leave the cursor there.

Other editing features allow you to quit the edit saving all changes, to quit deleting all changes or the cancel all changes made and restart.

All in all a very powerful editor.

Level two basic allows variables to be stored in different forms — single or double precision. A # (hash) after a calculation will cause the result to be output as a 16 place decimal (print 1/3 #), an ! (exclamation mark) will keep a variable at single precision (G!) and a % (percent) symbol will keep the number as an interger.

The system of representation can be selected for the type of work required — Tip, if you can work with whole numbers, store them as integers, your program will run twice as fast and occupy half the memory space as programs without these restrictions.

Print Format

Level 2 BASIC allows some fairly sophisticated formatting of output. It can be used in many applications such as printing report headings, accounting reports etc. Using nine "Field Seperators," one can specify digit positions, cause automatic rounding off, concatentate (join together) multiple strings or string variables, align columns — a comprehensive system that allows any output to be presented in an easily readable form.

Strings

The Level 2 manual states that "Without string handling capabilities, a computer is just a superpowered calculator." What this makes the level I machine with it's "String Things" — strings which one was not able to handle — is a question we leave open: Needless to say the Level 2 machine provides a wide range of string handling commands.

The DEFSTR statement allows any valid variable name to contain a string, adding a type declaration character ($) has the same effect.

Each string can contain up to 255 characters and strings can be compared as well as concatented.

Strings can be compared with the same symbols used for comparing numbers — The ASCII codes for the characters being the values compared.

Level 2 features ASC and CHR$ commands. ASC gives the ASCII numerical code for a string character while CHR$ performs the reverse operation.

The INKEYS$ function will allow an entry from the keyboard while a program is running — without the use of the return key, useful for "Real Time" games if nothing else. INKEY$ will strobe the keyboard and return with a one character string — This being a null string if no key is pressed.

Manipulation of strings can be carried out with the following commands, LEFT$, MID$, RIGHT$, LEN and string$.

LEFT$ (A$, 3) will print the first three characters from the left of A$ — Thus if A$ were TANDY the command would select TAN — MID$ will select characters from the middle of the string and RIGHT$, not surprisingly, from the right. The formats are the same for LEFT$.

STRING$ provides a string of a specified character for a specified charact4er for a specified length. For example STRING$ (25, ?) would output 25 question marks.

Trace

Level 2 BASIC provides a trace facility that is very useful for the debugging of programs. The command TRON followed by RUN will output to the screen the exact sequence in which the program lines are executed. To turn off the trace function the command TROFF is used.

Functions

Level 2 adds considerably to the four in- built arithmetic features of level 1.

To MEM (size of used memory), INT (Convert variable to interger), ABS (absolute value of variable) and RND (Random number generator) are added 12 more.

These are the trig functions SIN, COS, TAN and ATAN — The maths functions EXP, LOG, SGN and SQR.

The command RANDOM at the beginning of a program will ensure a different series of random numbers each time program is run. CDBL and CSNG concern themselves with the format in which a numerical variable is stored. CDBL provides a double-precision value of the expression following CDBL in brackets, even if the operands are single — precision or integers, CSNG does the oposite by providing a single precision value of the expression.

Many other functions may be created using the 16 Level 2 functions and Appendix E of the level two manual provides a guide to these.

Error messages

Whereas level 1 BASIC provides three (WHAT? HOW: and SORRY) error messages, Level 2 has 23 two-letter codes providing a far more specific indication of the error. Level I's feature of printing the error code at the exact point at which the error occurs is however lost — The message being printed on the following line.

Another facility present in Level 1 but lost in the more powerful version is that of abbreviated statements and commands.

Tandy, however, produce a conversion-tape which will allow software written for a Level 1 machine to be converted to run a Level 2 machine.

Arrays

Arrays are permitted to Level 2, the number of dimensions being limited by the size of the available memory-string arrays are also allowed.

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TRS-80 Update

Level 2 BASIC provides the PEEK and POKE commands. Poke allows a specified value to be written into a specific RAM location PEEK allows the value stored at any RAM location to be retrieved.

POKE is particularly useful when producing graphics displays on the screen. Level 1's SET statements for handling screen graphics were rather slow — Level 2 provides 63 special graphics characters that can be speedily manipulated by the POKE command.

The display can also produce double width (same height) characters. By hitting shift key and right arrow the format is changed. Note however that anything on screen at the time only has every other letter enlarged. The clear key will return the display to the normal format.

User Subroutines

Level 2 features the BASIC USR statement that permits the calling of user written subroutines.

File Search

Level 2 allows the user to label files and to search for a the named file. CLOAD "TEST" will ignore all files on tape until the one called "TEST" is found. A useful feature is that as the machine searches for a file, the names of all those on the tape before the specific file is found will be displayed in the upper right hand corner of the display.

A file can be verified after being dumped by entering CLOAD? The machine will then load the program from tape and compare it with that stored in memory. A handy feature that allows one to ensure a program has been faithfully recorded.

Manual

Unlike the level 1 manual, which makes an excellent job of teaching BASIC, the level 2 manual — As the forward says — Is not for the total beginner.

The manual does not go into the detail of the 233 page Level 1 manual. The Level 2 manual for example presents nine subroutines for array/matrix manipulation with very little explanation. 31 function codes are mentioned in one of appendices with little indication of how they are used in a program.

Computer Conclusion

The Level 2 package certainly provides a significant improvement over the Level 1 version of this machine and, in our opinion takes the TRS-80 from the realms of the "Superpowered Calculator" — Tandy's words — into the area of real computing.

The number of add-ons available now — floppies, memory, printers etc means that the TRS-80 can form the heart of a flexible system suitable for a wide range of applications.

See the article on a small business application of a TRS-80 system elsewhere in this issue.

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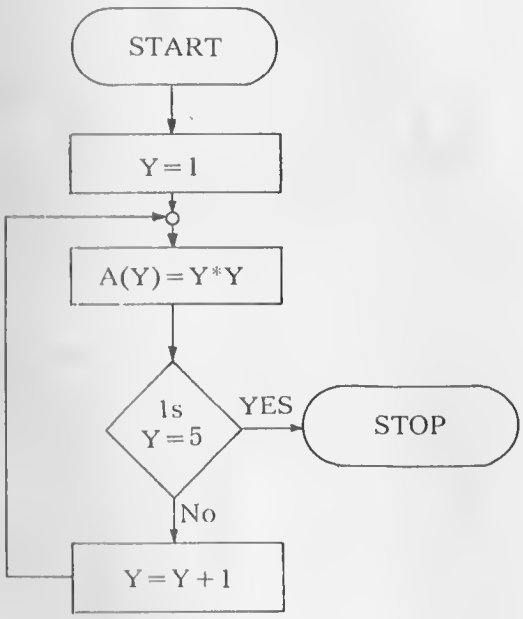
Beginning BASIC

Phil Cornes resumes his description of BASIC with a look at some of the conditional branching instructions featured in this language

IF THEN

THIS IS THE first of BASIC's really powerful conditional branching statements (we look at the others below) that go into the make-up of BASIC and we will add an IF THEN statement to the previous program segments (last month) to see what it can do.

Consider the following—



This is the same flow chart that we saw earlier except that now there is a two-way branch added which is made dependent upon the answer to the question 'IS Y=5'.

Before we go on to look at the program derived from this flow chart, there is one other thing we need to consider. You will notice from the flowchart that IF Y is 5 when the decision box asks the question THEN we branch to a stop box. The statement in BASIC which causes the execution of a program to terminate is the END statement and you will find one of these in the program.

There is no statement in BASIC which corresponds to the start box on the flow chart (that is just presented for our information) and so the first box we consider contains Y=1. The statement needed to convey this to the computer is—

LET Y=1

but remember that every statement in a program must have a line number, and so we have—

10 LET Y=1

We now move on to the next box

A(Y)=Y*Y and produce the statement—

20 LET A(Y)=Y*Y

The third box is the new one and we write—

30 IF Y=5 THEN

THEN what? Well, we have to branch to the line number which contains the END statement, but we don't yet know which one this will be. So we can either sit and wait until we have written the END statement, or we can say always let the END statement exist on some high numbered line (say 9999) so if we ever need an END statement, we know what line it will appear on. We will do it this way so line 30 will read—

30 IF Y=5 THEN 9999

and so if Y does equal 5, then we branch to the END statement that we will put in line 9999.

If the test (IS Y=5) fails (answer is NO) then line 30 will be ignored and the computer will carry on executing the statements in the normal line number order.

The next box down contains Y=Y+1, and so line 40 reads—

40 LET Y=Y+1

From this we now branch back to the statement A(Y)=Y*Y which is on line 20 and we get—

50 GOTO 20

and lastly

9999 END

If we write this out in line number order, we get—

```
10 LET Y = 1
20 LET A(Y) = Y*Y
30 IF Y = 5 THEN 9999
40 Y = Y + 1
50 GOTO 20
9999 END
```

and this is our first complete program.

It does not matter that the line numbers do not follow on in multiples of 10, they don't have to, but what is more important is the fact that we do leave some numbers spare between our statements so that if we find we have missed out a line, or think of something else that we would like to add, then we have plenty of space to do so.

Consider the following—

```
 5 REM INITIALIZE Y
10 LET Y = 1
15 REM PUT Y*Y IN A(Y)
20 LET A(Y) = Y*Y
25 REM TEST FOR Y = 5
30 IF Y = 5 THEN 9999
40 LET Y = Y + 1
50 GOTO 20
9999 END
```

REM (I thought REM was an android or something to do with sleep) in BASIC is short for REMark and tells the computer that whatever follows on this line is to be ignored because they are only notes for the programmer as a reminder of what is happening.

REMark statements in a program of this length are unnecessary, but we will soon be writing programs of sufficient length and complexity to justify their use as memory aids.

Returning now to our IF-THEN statement (IF Y = 5THEN 9999) the equals sign used here is not an arithmetic operator, but the first of the comparison operators. Any of the other comparison operators (<,>,<=,>=,<>) could also be used in an IF THEN statement, so that–

```
30 IF Y>4 THEN 9999
130 IF Q<19.2 THEN 55
902 IF A(17)>=14.9 THEN LET P = P + 1
```

are all valid statements.

Notice here the twist in the tail of line 902. This is also a valid statement on most machines. This is easier to understand if we consider the IF-THEN statement as two separate statements. The first part (the IF part) asks a question (in line 902 – IS A(17)>=14.9) to which the computer can answer either YES or NO. If the answer is NO then this statement is finished with and control passes on to the next higher numbered line. If the answer is YES then the computer passes on to the second statement on the line, the THEN part. THEN what? THEN LET P = P + 1 or THEN END or THEN 900 (this is really an abbreviation of THEN GOTO 900) or THEN any other statement. We can even put another IF THEN statement in.

Consider the following–

```
200 IF (A = 1)*(B = 1) THEN 900
```

The computer encountering this would first ask the question IS A = 1. If the answer is NO control passes to the next higher numbered line. If, on the other hand, A is equal to 1, we move on to the statement following the THEN and encounter another IF THEN statement which is treated in exactly the same way as the first. IS B = 1. If NO then carry on with the next line, if YES THEN GOTO 900. You will see that using this logic we will only pass control to line 900 if both A = 1 AND B = 1. At about this point your memory should be stirring to the fact that you have read something about logical operators earlier and indeed this is the place where they fit in. Depending on which machine you are considering, there are two ways of re-writing line 200 above to achieve the same result.

You could use–

```
200 IF A = 1 AND B = 1 THEN 900
```

which will normally be the format for machines with standard or extended BASIC, or–

```
200 IF (A = 11)*(B = 1) THEN 900
```

for the tiny BASIC machines

Notice the brackets in the second example. These tell the computer where one comparison ends and the other starts, otherwise the computer would attempt the following—

```
200 IF A = 1*B
```

(multiplication sign!) and then bomb out on the second equals sign.

The other common logical operator (OR) can also be used in a similar manner—

```
300 IF Q>3*H OR S<9 THEN R = R-2
```

or

```
300 IF (Q>3*H)+(S<9) THEN R = R-2
```

Notice the brackets again in the second example for similar reasons, and notice also the omission of the optional LET keyword before the R = R–2. We will continue to omit the LET from now on.

Finally for this month, we will go on to consider one of the pre-defined functions of BASIC (somewhat out of turn, but we'll see why in a moment) the random number generator.

RND(X)

We will start off by saying that the X within the brackets (the brackets are necessary and must be used whatever we replace X by) may be replaced by

»»

any constant, variable name or expression with the proviso that when the computer evaluates the contents of the brackets they must not be negative or the computer will bomb out. When the computer has evaluated the brackets and checked that the answer is not negative, anything after the decimal point is chopped off (so that 0.238 would be truncated to 0, similarly 8.9 would become 8 and so on) if the result of this operation is zero, then the computer will generate a random decimal number (up to 6 digits) in the range zero to one, so that when—

```
20 R=RND(0)
```

is executed, R will take a random value between zero and one. If the result after truncating the contents of the brackers is not zero (it must by now be a positive integer) then the computer will generate a random integer between one and the number in the brackets inclusive, so that, for example—

```
50 R=RND(6.8)
```

Would assign a random integer to R with a value between 1 and 6 inclusive (6.8 would be truncated to 6). This is a very useful function for any statistical or games applications and has been included at this time so that we can set you some homework (you need the practice). You will find that you now know enough about BASIC to convert the three card shuffling routines presented in the first part into programs and we would suggest that if you are following this series seriously, you should attempt to do just this. Sample answers will be presented next month.

The answers to the questions posed last month are

1 The expression has a value of 21, and
2 the expression could be simplified to
7+7*8/2/((12+8)*2/20)
You cannot remove the brackets round (12+8)*2/20 (if you made this mistake, think about why not).

Next month we go on to look at how we get the computer to print some answers, subroutines and some more conditional branching.

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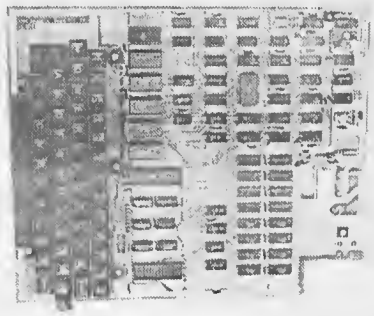

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Commands

CONT	LIST	NEW	NULL	RUN	

Statements

CLEAR	DATA	DEF	DIM	END	FOR
GOTO	GOSUB	IF...GOTO	IF...THEN	INPUT	LET
NEXT	ON...GOTO	ON...GOSUB	POKE	PRINT	READ
REM	RESTORE	RETURN	STOP		

Expressions

Operators

−, +, *, /, ↑, NOT, AND, OR, >, <, <>, >=, <=, =

RANGE 10^{-32} to 10^{+32}

Functions

ABS(X)	ATN(X)	COS(X)	EXP(X)	FRE(X)	INT(X)
LOG(X)	PEEK(I)	POS(I)	RND(X)	SGN(X)	SIN(X)
SPC(I)	SQR(X)	TAB(I)	TAN(X)	USR(I)	

String Functions

ASC(X$)	CHR$(I)	FRE(X$)	LEFT$(X$,I)	LEN(X$)	MID$ (X$,I,J).
RIGHT$(X$,I)			STR$(X)		VAL(X$)

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